

IN THE CLAIMS:

Claims 1 – 13 are currently pending in this application, wherein claims 14 – 15 are being withdrawn from further consideration without prejudice or disclaimer, as follows:

1. (Currently Amended) A liquid crystal display device comprising;
thin film transistors, scanning signal lines, data signal lines which are arranged in a state that the data signal lines intersect the scanning signal lines, pixel electrode[[s]] ~~which are~~ electrically connected to output electrode[[s]] of one of the thin film transistors, and common electrode[[s]] which form an electric field between ~~the common electrodes and the pixel electrode[[s]] on one substrate out of a pair of substrates which are arranged to face each other with liquid crystal there between, wherein,~~
[[in]] a pixel region which is surrounded by [[the]] neighboring two of the scanning signal lines and [[the]] neighboring two of the data signal lines,
wherein a metal heat diffusion member which is disposed in a spaced apart manner from the thin film transistor ~~is provided,~~
the heat diffusion member has a projecting portion ~~at a portion thereof~~ which is remoter than a distance between the thin film transistor and the heat diffusion member, and
~~at least one of the projecting portion[[s]] superpose with transparent electrode, and the transparent electrode is one of [[,]] the pixel electrode[[s]] and the common electrode[[s]] has superposed portions and at least one of the pixel electrodes and the common electrodes which are superposed on the projecting portions is formed of a transparent electrode at the superposed portion.~~
2. (Currently Amended) A liquid crystal display device according to claim 1, wherein a width of the projecting portion[[s]] is equal to or wider than a width of the pixel electrode[[s]] or the common electrode[[s]] at a portion[[s]] which superpose superposes the projecting portion.
3. (Currently Amended) A liquid crystal display device according to claim 2, wherein the pixel electrode[[s]] ~~are is formed of a transparent electrode and the heat diffusion~~

~~members~~ and the pixel electrode[[s]] are superposed each other at the projecting portion[[s]].

4. (Currently Amended) A liquid crystal display device according to claim 3, wherein the heat diffusing member[[s]] ~~are~~ is formed on the same layer as the output electrode[[s]] of the thin film transistors and the heat diffusion member[[s]] and the pixel electrode[[s]] are connected with each other via through holes formed in the heat diffusion member[[s]].
5. (Currently Amended) A liquid crystal display device according to claim 4, wherein the liquid crystal display device includes a common signal line[[s]], ~~and~~ the heat diffusion member[[s]] ~~are~~ superposed on the common signal line[[s]], and the projecting portion[[s]] of the heat diffusion member[[s]] ~~project~~ projects from the common signal line[[s]].
6. (Currently Amended) A liquid crystal display device according to claim 3, wherein the heat diffusion member[[s]] ~~also function as~~ is common signal line[[s]].
7. (Currently Amended) A liquid crystal display device according to claim 2, wherein the common electrode[[s]] ~~are~~ is formed of a transparent electrode and the heat diffusion member[[s]] and the common electrode[[s]] ~~are~~ superpose[[d]] each other at the projecting portion[[s]].
8. (Currently Amended) A liquid crystal display device according to claim 7, wherein the liquid crystal display device includes a common signal line[[s]] ~~and the common signal lines also function as the heat diffusion members,~~ wherein the heat diffusion member is the common signal line.
9. (Currently Amended) A liquid crystal display device comprising
thin film transistors, scanning signal lines, data signal lines which are arranged in a state that the data signal lines intersect the scanning signal lines, a pixel electrode[[s]] ~~which are~~ is connected to an output electrode[[s]] of one of the thin film transistors, and a common electrode[[s]] which ~~form~~ forms an

electric field between ~~the common electrodes and~~ the pixel electrode[[s]] ~~on one substrate out of a pair of substrates which are arranged to face each other with liquid crystal there between, wherein~~

[[in]] and a pixel region which is surrounded by [[the]] neighboring two of the scanning signal lines and [[the]] neighboring two of the data signal lines,

wherein a metal heat diffusion member ~~which~~ is disposed in a spaced apart manner from the thin film transistor [[is provided]], and

the heat diffusion member[[s]] ~~are~~ is arranged in an isolated manner, and ~~form~~ forms a superposed portion[[s]] together with [[at least]] one of the pixel electrode[[s]] and the common electrode[[s]].

10. (Currently Amended) A liquid crystal display device according to claim 9, wherein [[at least either]] the one of the ~~superposed pixel~~ electrode[[s]] and the common electrode[[s]] ~~are formed of a~~ is transparent [[electrode]].
11. (Currently Amended) A liquid crystal display device according to claim 1, wherein an inorganic insulation film and an organic insulation film are provided between a layer on which the heat diffusion member[[s]] ~~are~~ is formed and a layer on which an electrode[[s]] ~~which are~~ is superpose[[d on]] with the heat diffusion member[[s]] ~~are~~ is formed, and the organic insulation film has a [[removal]] removed portion [[at]] at least [[one portions of]] at the superposed portion[[s]] between the heat diffusion member[[s]] and the electrode[[s]].
12. (Original) A display device being characterized in that the display device include, a metal heat diffusion member which is superposed on a lower layer of a transparent electrode by way of an insulation film, the heat diffusion member has a projecting portion at a portion thereof remoter than a distance between the heat diffusion member and the thin film transistor, and the heat diffusion member is superposed on the transparent electrode at the projecting portion.
13. (Currently Amended) A display device according to claim 12, wherein an inorganic insulation film and an organic insulation film are provided between a layer on which the heat diffusion member[[s]] ~~are~~ is formed and a layer on which the electrode[[s]] ~~which are~~ superposed [[on]] with the heat diffusion

member[[s]] ~~are~~ is formed, and the organic insulation film has a ~~[[removal]]~~ removed portion ~~[[at]]~~ at least ~~one portions of~~ at the superposed portion[[s]] between the heat diffusion member[[s]] and the electrode[[s]]

14. (Withdrawn) A manufacturing method of a display device being characterized in that the method manufactures a display device which includes a metal heat diffusion member which is superposed on a lower layer of a transparent electrode by way of an insulation film, and the heat diffusion member has a projecting portion at a portion thereof remoter than a distance between the heat diffusion member and the thin film translator, wherein the heat diffusion member is superposed on the transparent electrode at the projecting portion, and the heat diffusion member and the transparent electrode are cut at the projecting portion so as to repair a short-circuit.
15. (Withdrawn) A manufacturing method of a display device according to claim 14, wherein the projecting portion and the transparent electrode are cut by heating the projecting portion with laser beams and, at the same time, the transfer of heat to the thin film transistor at the time of cutting is suppressed by the metal heat diffusion member.